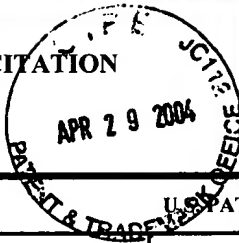


INFORMATION DISCLOSURE CITATION

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Docket Number (Optional)	Application Number
JP919996276US1	09/738,165
Applicant(s)	
Tamiya Onodera	
Filing Date	Group Art Unit
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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

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FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
SM	a	03-288933	04/06/2000	Japan - Patent Abstract	G06F	9/46	✓	
SM	b	2000-076086	08/31/1998	Japan - Patent Abstract	G06F	9/46	✓	
SM	c	05-225149	02/13/1992	Japan - Patent Abstract	G06F	15/16	✓	
SM	d	58-169659	03/30/1982	Japan - Patent Abstract	G06F	15/16	✓	
SM	e	11-327934	04/20/1999	Japan - Patent Abstract	G06F	9/46	✓	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

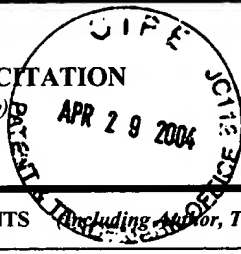
SM	f	Mauro, Jim. "Multiprocessing and Synchronizing Object," SunWorld, Vol. 10, No. 5, Pg. 112-121, IDG Japan Inc., May 1, 2000.
SM	g	Takada, Hiroaki, et al. "Intra-Processor vs. Inter-Processor Synchronizations in Real-Time Multiprocessor Systems," Technical Report of IEICE, Vol. 92, No. 497, p. 41-48.

EXAMINER	DATE CONSIDERED
	5/19/04

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*EXAMINER
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h Yoshimasa, Kawabe. "Performance Improvement by Reconstructing Mechanism of Lock and Memory Managements," Nikkei Linux, Pre-Establishment Issues, pp. 111-121.

i CPSY92-85, The Institute of Electronics, Information, and Communication Engineers, March 12, 1993.

j "Weak Locks with Two-Level Locking Multi-Computer System Protocol to Reduce Lock-Holding Time," IBM Technical Disclosure Bulletin, Vol. 33, No. 38, August 1990, pp. 287-289, IBM Corp., XP00124354.

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